

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lai, *et al.* Docket No.: TSM03-0199
Serial No: 10/783,498 Art Unit: 2814
Date Filed February 20, 2004 Examiner: Phat X Cao
Title: Phase Change Memory Cell and Method of Its Manufacture

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Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

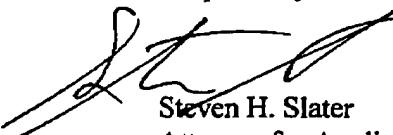
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12/20/2005 HLE333 00000029 501065 10783498
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Respectfully submitted,



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December 19, 2005

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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(Use as many sheets as necessary)</i></p>				<p>Complete If Known</p>	
				Application Number	10/783,498
				Filing Date	02/20/2004
				First Named Inventor	Lai, et al.
				Art Unit	2814
				Examiner Name	Phat X Cao
Sheet	1	of	1	Attorney Docket Number	TSM03-0199

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	HA, Y.H., et al., "An Edge Contact Type Cell for Phase Change RAM Featuring Very Low Power Consumption," 2003 Symposium on VLSI Technology Digest of Technical Papers, pp. 175-176.	

Examiner Signature		Date Considered	
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